



# St. Martin's Engineering College

An Autonomous Institution  
NBA & NAAC A+ Accredited  
Dhulapally, Secunderabad-500 100  
www.smec.ac.in



## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### TIME - TABLE

Year and Sem : B.Tech II year I Sem A. Y : 2020-2021  
Section : A W.e.f : 08.02.2021  
Class Incharge : Mrs.G. Lakshmi Priyanka

TIMINGS/ Hour Day	9:20 AM To 11:20 AM	11:30 AM To 1:30 PM	1:30 PM To 2:00 PM	2:00 PM To 4:00 PM
<b>MONDAY</b> (08-02-2021)	BS (B1) / DSD (B2)	DSD(B1) / EDC (B2)	LUNCH BREAK	EDC (B1) / BS (B2)
<b>TUESDAY</b> (09-02-2021)	EDC (B2)	RECORD WRITING		RECORD WRITING
<b>WEDNESDAY</b> (10-02-2021)	RECORD WRITING	REVISION (SS)		EDC(B1)
<b>THURSDAY</b> (11-02-2021)	RECORD WRITING	REVISION (DSD)		RECORD WRITING
<b>FRIDAY</b> (12-02-2021)	DSD (B1) / BS(B2)	BS(B1) / DSD(B2)		RECORD WRITING
<b>SATURDAY</b> (13-02-2021)	REVISION (EDC)	RECORD WRITING		REVISION (NATL)

<b>BS LAB</b>	Basic Simulation Lab	BS	Mrs G.Lakshmi Priyanka
<b>DSD LAB</b>	Digital System Design Lab	DSD	Mr.Ramana Rao
<b>EDC LAB</b>	Electronic Devices & Circuit Lab		Mr. M. Thirupathi
			Mr.G.Anjaneyulu
			Ms.B.S.Sucharitha
		EDC	Ms.P.Pushpa
			Mrs.T.SriLaxmi
			Mr.K.Nishakar
<b>SS</b>	Signals and Systems	SS	Mrs G.Lakshmi Priyanka
<b>PTSP</b>	Probability Theory and Stochastic Processes	PTSP	N.Viswanath
<b>DSD</b>	Digital SystemDesign	DSD	M.Thirupathi
<b>EDC</b>	Electronic Devices and Circuits	EDC	P.Pushpa
<b>NATL</b>	Network Analysis and Transmission Lines	NATL	T.V.Sai Kalyani

TIME TABLE COORDINATOR	HOD-ECE	PRINCIPAL

Copy to: Principal, Chief In Charge [Time Tables], HOD's - EEE, CSE, CIVIL, ECE, IT, MECH, S & H, MBA







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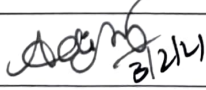

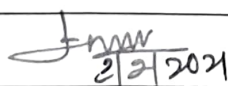
## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### TIME - TABLE

**Year and Sem** : B.Tech II Year I Sem      **A. Y** : 2020-2021  
**Section** : D  
**Class Incharge** : Mr.N.Vishawanath      **W.e.f** : 08.02.2021

TIMINGS/ HOUR Day	9:20 AM To 11:20 AM	11:30 AM To 1:30 PM	1:30 PM To 2:00 PM	2:00 PM To 4:00 PM
<b>MONDAY</b> (08-02-2021)	REVISION (DSD)	RECORD WRITING	LUNCH BREAK	REVISION (SS)
<b>TUESDAY</b> (09-02-2021)	REVISION (EDC)	RECORD WRITING		EDC (B1)
<b>WEDNESDAY</b> (10-02-2021)	DSD (B1)	BS (B1)		RECORD WRITING
<b>THURSDAY</b> (11-02-2021)	EDC (B1)	BS (B1) / DSD (B2)		REVISION (PTSP)
<b>FRIDAY</b> (12-02-2021)	EDC (B2)	RECORD WRITING		BS (B2)
<b>SATURDAY</b> (13-02-2021)	BS (B2)	DSD (B1)/EDC (B2)		DSD (B2)

<b>BS LAB</b>	Basic Simulation Lab	BS	Dr.A.Chaitanya Krishna
<b>DSD LAB</b>	Digital System Design Lab	DSD	Mr.N.Vishwanath
<b>EDC LAB</b>	Electronic Devices & Circuit Lab		Mr. S.P.ManiKanta
			Mr.K.Nishakar
		EDC	Mr.D.Prasad
			Mr.K.N.V.Rao
			Mrs.T.SriLaxmi
<b>PTSP</b>	Probability Theory and Stochastic Processes	PTSP	Mrs. P.Kiranmayee
<b>DSD</b>	Digital System Design		N.Vishwanath
<b>EDC</b>	Electronic Devices and Circuits		S.P.Manikanta
<b>SS</b>	Signals and Systems		B.Ravi Chander
			SS

 2/2/21		 2/2/2021
TIME TABLE COORDINATOR	HOD-ECE	PRINCIPAL

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